Product Features

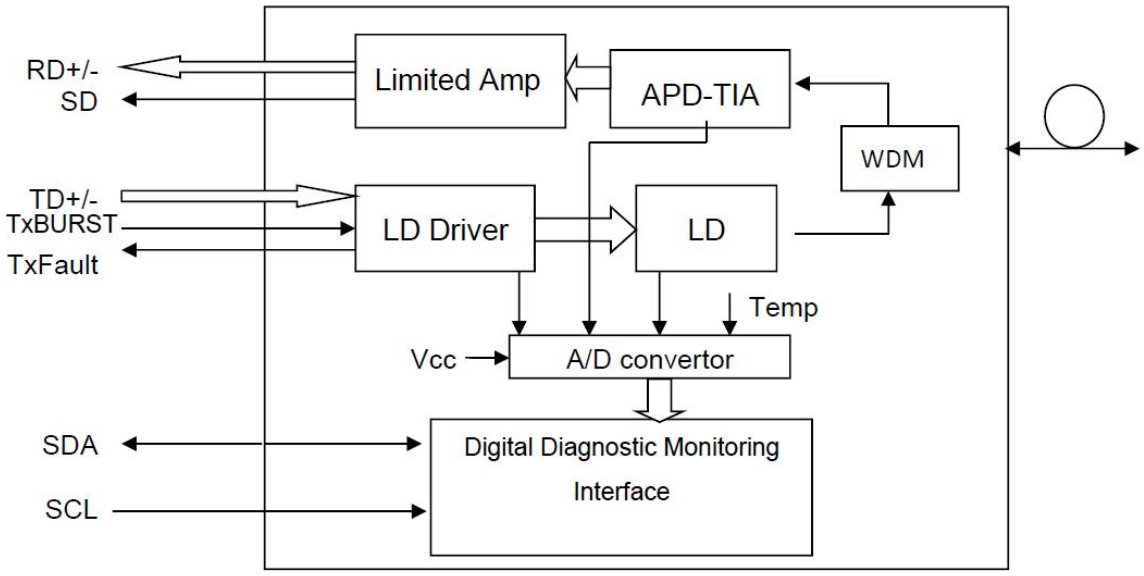
- ✧ BiDi SFP Single Mode Transceiver
- ✧ SC receptacle is optional
- ✧ Comply with ITU-T G.984.2 Class B+
- ✧ Compliant with SFF MSA-2000 And SFF-8472 V10.3
- ✧ Single +3.3 Power Supply
- ✧ LVPECL Differential Data Inputs And CML Data Outputs
- ✧ LVTTL Signal Detection Output And LVTTL Burst Control
- ✧ Complies with Telcordia (Bellcore) GR-468-CORE
- ✧ 1310 nm Burst Mode Transmitter and 1490 nm Continuous Mode Receiver
- ✧ 1.244 Gbps DFB Laser Diode, 2.488 Gbps APD-TIA Receiver
- ✧ Maximal reach 20km

Applications

- ✧ GPON ONU For P2MP Application

General

The GPON ONU SFP Class B+ transceiver with BIDI SFP package supports typically 1.244 Gbps Tx,2.488 Gbps Rx Asymetric Data Rate for GPON ONU application up to 20km transmission distance, it's designed meeting with ITU-T G.984.2 Class B+. SC rececptacle is for optical interface



Performance Specifications

Optical Specification						
Transmitter						
Parameter	**Symbol**	**Min.**	**Typ.**	**Max.**	**Unit**	**Note**
Data Rate	DR	-	1.244	-	Gbps	-
Optical Central Wavelength	λ	1260	1310	1360	nm	
Spectral Width (-20dB)	Δλ	-	-	1	nm	
Side Mode Suppression Ratio	SMSR	30	-	-	dB	
Average Optical Output Power	Po	0.5	-	5	dBm	
Extinction Ratio	Er	9	-	-	dB	-
Tx Burst ON Time	Ton	-	-	12.86	ns	-
Tx Burst OFF Time	Toff	-	-	12.86	ns	-
Rise/Fall Time	Tr/Tf	-	-	250	ps	-
Average Lauched Power of Off Transmitter	Poff	-	-	-45	dBm	-
Output Eye	Compliant with ITU-T G.984.2					
Receiver						
Parameter	**Symbol**	**Min.**	**Typ.**	**Max.**	**Unit**	**Note**
Data Rate	DR	-	2.488	-	Gbps	-
Operate Wavelength	-	1480	-	1500	nm	-
Sensitivity	Pr	-	-	-28	dBm	1
Saturation	Ps	-8	-	-	dBm	1
SD De-assert Level	-	-45	-	-	dBm	-
SD Assert Level	-	-	-	-28	dBm	-
SD Hysteresis	-	0.5	-	6	dB	-
Optical Return Loss	-	-	-	-12	dB	-
RSSI Range	-	-28	-	-8	dBm	-
RSSI Accuracy	-	-3	-	+3	dB	-

Note:

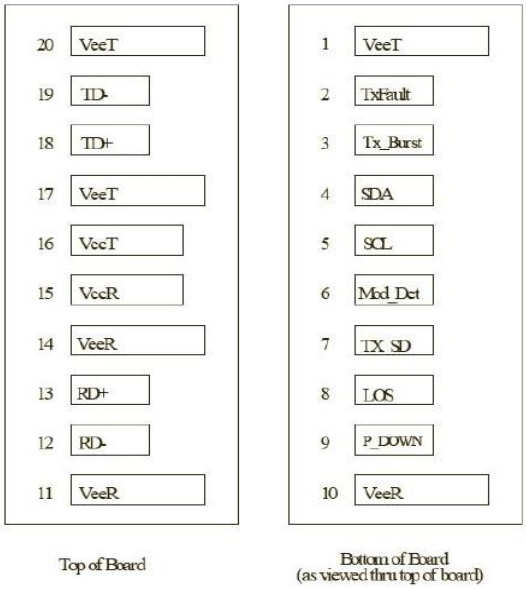
1. Minimum Sensitivity and saturation levels for a 2^{23}-1 PRBS. BER≤10^{-10}, 2.488Gpbs, ER=9dB

Electrical Specification						
Transmitter						
Parameter	**Symbol**	**Min.**	**Typ.**	**Max.**	**Unit**	**Note**
Differential Input Voltage	V_{IN-DIF}	300	-	1600	mV	-
Tx Burst Input Voltage-Low	V_{IL}	0	-	0.8	V	-
Tx Burst Input Voltage-High	V_{IH}	2.0	-	Vcc	V	-
Receiver						
Parameter	**Symbol**	**Min.**	**Typ.**	**Max.**	**Unit**	**Note**
Data Output Voltage Differential	$V_{OUT-DIF}$	500	-	900	V	-
Signal Detect Output Voltage-Low	V_{SD-L}	0	-	0.8	V	-
Signal Detect Output Voltage-High	V_{SD-H}	2.0	-	Vcc	V	

Digital Diagnostic Monitor Accuracy

Parameter	Unit	Accuracy	Range	Calibration
Tx Optical Power	dB	±3	Po: -Pomin~Pomax dBm, Recommended operation conditions	External/Internal
Rx Optical Power	dB	±3	Pi: Ps~Pr dBm, Recommended operation conditions	External/Internal
Bias Current	%	±10	Id: 1-100mA, Recommended operating conditions	External/Internal
Power Supply Voltage	%	±3	Recommended operating conditions	External/Internal
Internal Temperature	℃	±3	Recommended operating conditions	External/Internal

PIN Diagram



PIN Description

Pin No.	Name	Function	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	
2	Tx Fault	Transmitter Fault Indication	3	Note 1
3	Tx Burst	Transmitter Burst Mode Control.	3	Note 2
4	SDA	Module Definition 2	3	Note 3
5	SCL	Module Definition 1	3	Note 3
6	MOD-DET	Module Definition 0	3	Note 3
7	TX SD	Tx Transmitter State Indication, assert When Tx ON . Optional	3	
8	LOS	Los Of Signal	3	Note 4
9	P_DOWN	Power Down, NC/High=Normal operation ,Low=Power down. Optional	1	
10	VeeR	Receiver Ground	1	Note 5
11	VeeR	Receiver Ground	1	Note 5
12	RD-	Inv. Receiver Data Out	3	Note 6
13	RD+	Receiver Data Out	3	Note 6
14	VeeR	Receiver Ground	1	Note 5
15	VccR	Receiver Power Supply	2	Note 7, 3.3V± 5%
16	VccT	Transmitter Power Supply	2	Note 7, 3.3V± 5%
17	VeeT	Transmitter Ground	1	Note 5
18	TD+	Transmitter Data In	3	Note 8
19	TD-	Inv.Transmitter Data In	3	Note 8
20	VeeT	Transmitter Ground	1	Note 5

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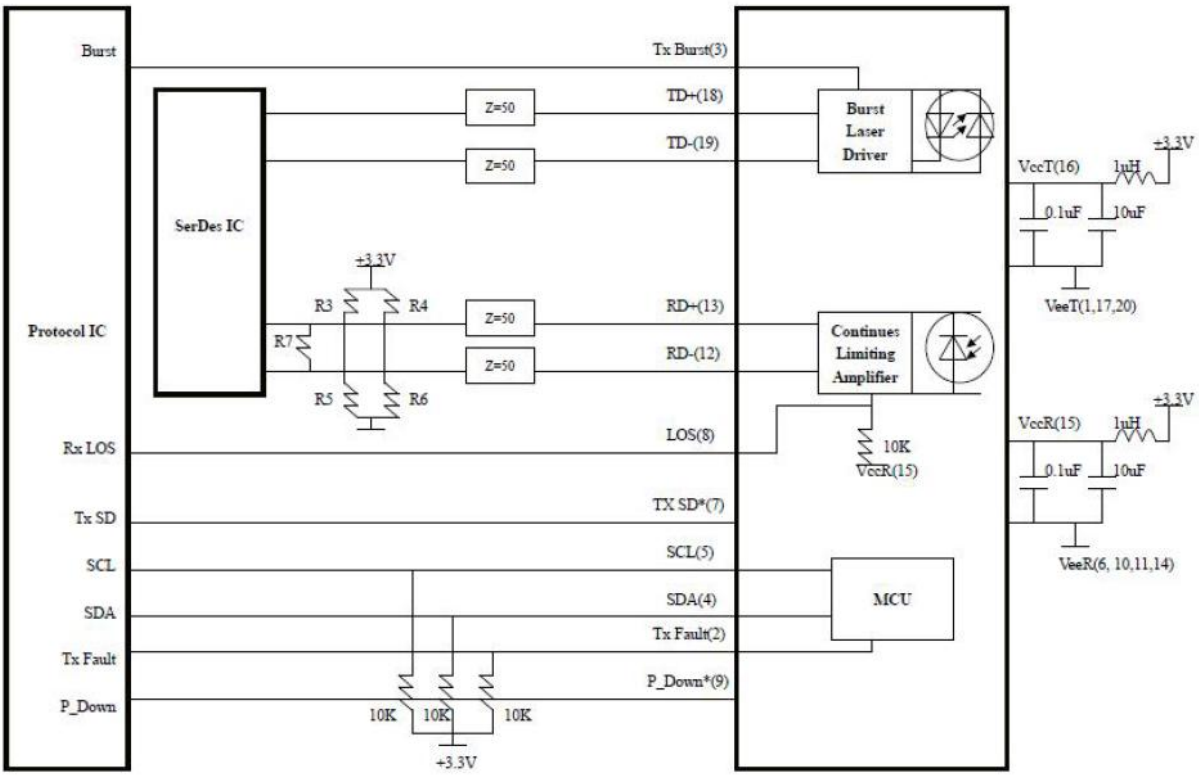
Notes:

1. TX Fault is an open collector/drain output, which should be pulled up with a 4.7K–10KΩ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
2. TX Burst is an input that is used to enable/disable the transmitter optical output.
   Burst Logic '1' or Logic '0' Tx on ,pleaser refer to order information
   Logic '0' Low 0 – 0.8V
   >0.8, < 2.0V Undefined
   Logic '1' High 2.0 – 3.465V
   Open Undefined
3. Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K – 10KΩ resistor on the host board. The pull-up voltage shall be VccT or VccR.
   Mod-Def 0 is grounded by the module to indicate that the module is present
   Mod-Def 1 is the clock line of two wire serial interface for serial ID
   Mod-Def 2 is the data line of two wire serial interface for serial ID
4. LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
5. VeeR and VeeT may be internally connected within the SFP module.
6. RD-/+: These are the differential receiver outputs. They are DC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES.
7. VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V 5% at the SFP connector pin. Maximum supply current is 450mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1Ω should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA greater than the steady state value. VccR and VccT
may be internally connected within the SFP transceiver module.
8. TD-/+: These are the differential transmitter inputs. They are DC-coupled, differential lines with 100Ω differential termination inside the module.

Recommended Circuit



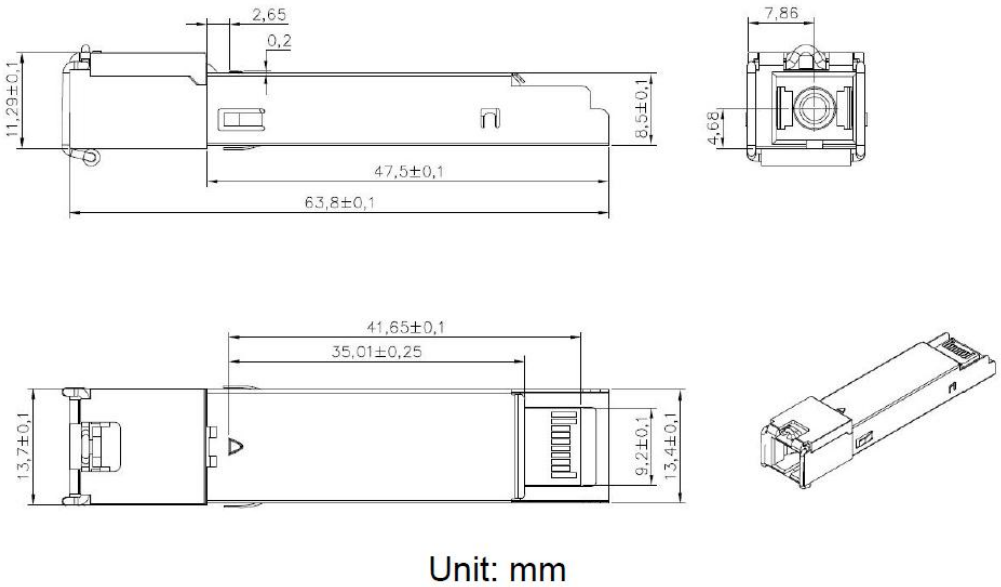
Note:

Tx：DC coupled internally.

Rx：AC coupled internally.

Input stage in SerDes IC with internal bias to Vcc-1.3V R3=R4=R5=R6=N.C, R7=100Ω Input stage in SerDes IC without internal bias to Vcc-1.3V R3=R4=82Ω,R5=R6=130Ω,R7=N.C

Package Diagram



Unit: mm